DAFTAR PUSTAKA

Chu, Pong P. 2008. *FPGA Prototyping By VHDL Examples: Xilinx Spartan-3 Version*. New Jersey: John Wiley & Sons, Inc.

Deschamps, Jean-Pierre dkk. 2006. *Synthesis of Arithmetic Circuit: FPGA, ASIC, and Embedded Systems*. New Jersey: John Wiley & Sons, Inc.

Kilts, Steve. 2007. *Advanced FPGA Design Architecture, Implementation, and Optimization*. New York: John Wiley & Sons, Inc.

Muller, Jean-Michel dkk. 2010. *Handbook of Floating-Point Arithmetic*. Boston: Birkhäuser.

Navabi, Zainalabedin. 2005. *Digital Design and Implementation with Field Programmable Devices*. New York: Kluwer Academic Publishers.

Overton, Michael L. 2001. *Numerical Computing with IEEE Floating Point Arithmetic*. New York: SIAM.

Perry, Douglas L. 2002. *VHDL: Programming by Example*. New York: McGraw-Hill.

Sass, Ronald dkk. 2010. *Embedded Systems Design with Platform FPGAs: Principles and Practices*. Amsterdam: Morgan Kaufmann Publishers.

Xilinx, Inc. 2009. *Constraints Guide*. California: Xilinx, Inc.

_____. 2009. *ISE In-Depth Tutorial*. California: Xilinx, Inc.

_____. 2009. *Spartan-3E Libraries Guide for HDL Designs*. California: Xilinx, Inc.

_____. 2009. *XST User Guide*. California: Xilinx, Inc.