

**FPGA IMPLEMENTATION OF ZIEGLER-NICHOLS CLOSED-LOOP METHOD FOR
AUTOMATIC PID PARAMETERS TUNING**

FINAL PROJECT

has been submitted in partial fulfillment of the requirements for the
degree of Bachelor Engineering (*Sarjana Teknik*)



By:

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MINISTRY OF EDUCATION AND CULTURE

BRAWIJAYA UNIVERSITY

FACULTY OF ENGINEERING

DEPARTMENT OF ELECTRICAL ENGINEERING

MALANG

2015

APPROVAL SHEET

**FPGA IMPLEMENTATION OF ZIEGLER-NICHOLS CLOSED-LOOP
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ELECTRICAL ENGINEERING DEPARTMENT**

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CONFIRMATION SHEET

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**FINAL PROJECT
ELECTRICAL ENGINEERING DEPARTMENT**

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This final project report has been defended and rewarded for acceptance and approval
for the degree of Bachelor Engineering on
26 March 2015

EXAMINER COMMITTEE

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